

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Danial S. Dean

Serial No.: Not yet assigned

Filed: August 4, 2003

For: DEVICE FOR MARGIN TESTING A
SEMICONDUCTOR MEMORY BY
APPLYING A STRESSING VOLTAGE
SIMULTANEOUSLY TO
COMPLEMENTARY AND TRUE DIGIT
LINES

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2269-3318.6US
(97-0527.06/US)

NOTICE OF EXPRESS MAILING

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The present application is a continuation of application Serial No. 10/212,903, filed August 5, 2002, pending, which is a continuation of application Serial No. 09/977,755, filed October 15, 2001, now U.S. Patent 6,442,086, issued August 27, 2002, which is a continuation of application Serial No. 09/897,360, filed July 2, 2001, now U.S. Patent 6,327,201, issued December 4, 2001, which is a continuation of application Serial No. 09/583,478, filed May 31, 2000, now U.S. Patent 6,256,242, issued July 3, 2001, which is a continuation of application Serial No. 09/392,154, filed September 8, 1999, now U.S. Patent 6,101,139, issued August 8,

2000, which is continuation of application Serial No. 09/026,244, filed February 19, 1998, now U.S. Patent 6,002,622, issued December 14, 1999.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 or PTO/SB/08 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: August 4, 2003
JRD/sls:djp

Enclosures: Form PTO-1449 or PTO/SB/08
Document in ProLaw

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) 3318.6US (97-0527.06/US)	Application Number Not yet assigned
	Applicant Danial S. Dean	
	Filing Date August 4, 2003	Group Art Unit Unknown

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,208,778	05/1993	Kumanoya			
	5,265,056	11/1993	Butler			
	5,297,087	03/1994	Porter			
	5,500,824	03/1996	Fink			
	5,544,108	08/1996	Thomann			
	5,610,867	03/1997	Debrosse			
	5,710,740	01/1998	Manning			
	5,822,258	10/1998	Casper			
	5,903,502	05/1999	Porter			
	5,959,913	09/1999	Raad			
	5,982,682	11/1999	Nevill et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

		Prince, B., "Semiconductor Memories: A Handbook of Design, Manufacture and Application," (Wiley 1991, reprinted 1996).

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance. Include copy of this form with next communication to the applicant.

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>			Docket Number (Optional) 3318.6US (97-0527.06/US)		Application Number Not yet assign d	
			Applicant Danial S. Dean			
			Filing Date August 4, 2003		Group Art Unit Unknown	

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,002,622	12/1999	Dean			
	6,055,199	04/2000	Hamade et al.			
	6,061,285	05/2000	Tsukikawa			
	6,088,819	07/2000	Adachi et al.			
	6,101,139	08/2000	Dean			
	6,154,401	11/2000	Casper et al.			
	6,256,242	07/2001	Dean			
	6,327,201	12/2001	Dean			
	6,442,086	08/2002	Dean			

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